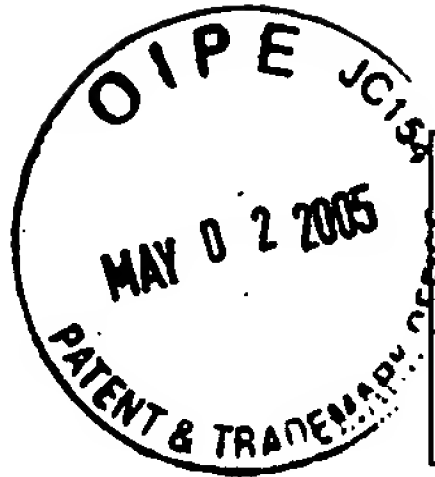


503-05

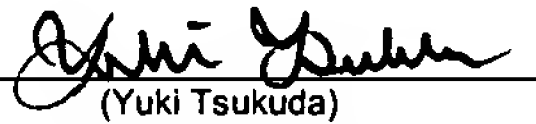
DFW



I hereby certify that this correspondence is being deposited with the U.S. Postal Service as Express Mail, Airbill No. EV644881883US, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: May 2, 2005

Signature:

  
(Yuki Tsukuda)

Docket No.: 03226/035001; P5030  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
David M. Pini et al.

Application No.: 09/994,479

Confirmation Number: 7240

Filed: November 27, 2001

Art Unit: 2111

For: SIGN GENERATION BYPASS PATH TO  
ALIGNER FOR REDUCING SIGNED DATA  
LOAD LATENCY

Examiner: K. S. Kim

**SUBMISSION OF FORMAL DRAWINGS**

MS Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Examiner's request in the Notice of Allowance dated April 11, 2005, submitted herewith is one set (eight sheets, eight figures) of formal drawings for filing in the above-identified patent application.

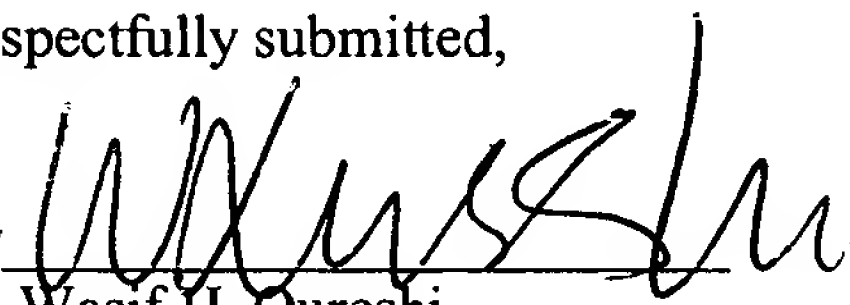
Kindly substitute the enclosed formal drawings for the drawings presently in the application.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 50-0591, under Order No. 03226/035001 from which the undersigned is authorized to draw.

Dated: May 2, 2005

Respectfully submitted,

By



Wasif H. Qureshi

Registration No.: 51,048

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